



By **CTU in Prague**
in cooperation with



WORKSHOP CHAIRS

HANA KUBÁTOVÁ, CTU IN PRAGUE (CZ)
PETR FIŠER, CTU IN PRAGUE (CZ)

STUDENT POSTER SESSION CO-CHAIRS

JOSEF KOUMAR, CTU IN PRAGUE, PRAGUE (CZ)
JAROSLAV PEŠEK, CTU IN PRAGUE, PRAGUE (CZ)

PROGRAMME COMMITTEE

A. BERNASCONI, UNIVERSITÀ DI PISA (IT)
J. BĚLOHOUBEK, CTU IN PRAGUE, FIT (CZ)
A. BOSIO, ÉCOLE CENTRALE DE LYON (FR)
L. CASSANO, POLITECNICO DI MILANO (IT)
G. DINATALE, TIMA, GRENOBLE (FR)
J.L. GAUDIOT, UNIVERSITY OF CALIFORNIA, IRVINE (USA)
V. JANIČEK, CTU IN PRAGUE, FEE (CZ)
K. JELEMENSKÁ, STU BRATISLAVA (SK)
M. JENIHHIN, TALLINN UNIV. OF TECHNOLOGY (EE)
P. KITSOS, TEI WEST. GREECE (GR)
M. KRSTIĆ, IHP, FRANKFURT (ODER) (DE)
R. KVAČEK, ASICENTRUM, PRAGUE (CZ)
F. LEPORATI, UNIV. DI PAVIA (GR)
I. LEVIN, TEL-AVIV UNIVERSITY (IL)
A. MCEWAN, UNIVERSITY OF LEICESTER (UK)
P. MRÓZ, UNIVERSITY OF ZIELONA GÓRA (PL)
M. NOVOTNÝ, CTU IN PRAGUE, FIT (CZ)
A. ORALOGLU, UC SAN DIEGO (USA)
Z. PLÍVA, TU LIBEREC (CZ)
M. POUPA, UNIVERSITY OF WEST BOHEMIA, FEE (CZ)
P. PUSCHNER, VIENNA UNIVERSITY OF TECHNOLOGY (AT)
J. RAIK, TALLINN UNIV. OF TECHNOLOGY (EE)
J. SCHMIDT, CTU IN PRAGUE, FIT (CZ)
M. SKRBK, CTU IN PRAGUE, FIT (CZ)
J. SOBOTKA, CTU IN PRAGUE, FEE (CZ)
B. STEINBACH, TU CHEMNITZ (DE)
A. STEININGER, VIENNA UNIVERSITY OF TECHNOLOGY (AT)
J. STRNADEL, BRNO UNIVERSITY OF TECHNOLOGY, FIT (CZ)
Z. VAŠIČEK, BRNO UNIVERSITY OF TECHNOLOGY, FIT (CZ)
W. ZAJĄC, JACOB OF PARADIES UNIVERSITY (PL)

ORGANIZING COMMITTEE

H. KUBÁTOVÁ, CTU IN PRAGUE, PRAGUE (CZ)
P. FIŠER, CTU IN PRAGUE, PRAGUE (CZ)
J. BORECKÝ, CTU IN PRAGUE (CZ)
M. NOVOTNÝ, CTU IN PRAGUE, PRAGUE (CZ)

SECRETARIAT

R. KINC, AMCA (CZ)
B. DUFKOVÁ, AMCA (CZ)

CONTACT INFORMATION

HANA KUBÁTOVÁ
PETR FIŠER
Department of Digital Design
Faculty of Information Technology
Czech Technical University in Prague
Thákurova 9, Praha 6, 160 00
Czech Republic
Tel.: +420 224 359 840
e-mail: kubatova@fit.cvut.cz, fiserp@fit.cvut.cz

WORKSHOP WEB PAGE

<http://pesw.fit.cvut.cz/>

Call for papers

12th Prague Embedded Systems Workshop

PESW 2024

June 27 – 29, 2024, Horoměřice, Czech Republic

**A forum for friendly discussion of emerging ideas
aimed at doctoral and prospective doctoral students**

BACKGROUND

The Prague Embedded Systems Workshop is an annual research meeting intended for the presentation and discussion of students' results and progress in all aspects of embedded systems design, testing, communications, and applications. It will be traditionally organized by the Department of Digital Design at the Faculty of Information Technology of the Czech Technical University in Prague. The workshop aims to enhance collaboration between different universities, not only inside the EU. It will be based on oral presentations, mutual communication, and discussions.

SCOPE

The PESW workshop addresses emerging issues, hot problems, new solution methods, and their hardware and software implementations in the fields of digital and mixed-signal system design. It is especially focused on dependable and low power design, and testing methods related to the SoC technology and modern embedded applications. The workshop topics include (but are not limited to):

- Programmable/re-configurable/adaptable architectures
- SoC and NoC design and testing
- Digital design optimization methods, logic synthesis and optimization
- Architectures and hardware for security applications
- On-line and off-line error detection and correction
- Fault-tolerant systems design methods, testability and reliability analysis
- Diagnosis, testing, and verification of embedded systems
- Mission-critical and real-time systems
- Heterogeneous Cyber-Physical Systems, robotic control
- Analog, mixed-mode, and physical synthesis and optimization
- Artificial intelligence, machine learning, neural networks
- Applications of (embedded) digital systems
- Embedded HCI

Special Session on Network Security:

- Network monitoring and measurements
- Network traffic processing and analysis, anomaly detection
- Security and privacy issues
- Security issues of Internet of Things

SUBMISSION TYPES

- **Full papers** (up to 10 pages, single-column) describing the student's original research. These papers will undergo a standard reviewing process and if accepted, they will be included in the proceedings with ISBN.
- **Abstracts** (up to four pages, single-column) of author's earlier published and successfully presented papers (at conferences, journals, etc.). These contributions will not be reviewed; emphasis will be put on the presentation and discussion. The abstracts will be included in the proceedings with ISBN.
- **Student posters** – abstracts of defended Bc. and MSc. Theses with subsequent poster presentation. These abstracts will not be included in the Proceedings.

SUBMISSION AND PRESENTATION

Prospective authors are encouraged to submit their contributions **before the submission deadline**. For details see the [PESW webpage](#).

Accepted full papers, abstracts, and student posters are supposed to be presented at PESW 2024 by the authors. Submitted abstracts will have longer presentations (20 min. presentation + 10 min. discussion).

The Proceedings will be distributed electronically and provided with ISBN. The list of papers and abstracts will be published at the PESW web page.

IMPORTANT DATES

Submission of full papers:	June 1, 2024
Submission of abstracts:	June 16, 2024
Notification of acceptance of full papers and abstracts:	June 18, 2024
Final versions of full papers and abstracts:	June 20, 2024
Submission of abstracts of student posters:	June 24, 2024
Notification of acceptance of student posters:	June 25, 2024
Workshop:	June 27 – 29, 2024