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WORKSHOP WEB PAGE
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Call for papers
7th Prague Embedded Systems Workshop 2019
PESW 2019
June 27–29, 2019, Roztoky u Prahy, Czech Republic
**A forum for friendly discussion of emerging ideas
aimed at doctoral and prospective doctoral students**

BACKGROUND
The Prague Embedded Systems Workshop is a research meeting intended for the presentation and discussion of students' results and progress in all aspects of embedded systems design, testing and applications. It is organized by members of the Department of Digital Design at Faculty of Information Technology (which is the youngest faculty) of the Czech Technical University in Prague (which is the oldest technical university in Central Europe). The workshop aims to enhance collaboration between different universities not only inside EU. It will be based on oral presentations, mutual communication and discussions.

SUBMISSION TYPES
- **Full papers** (up to 10 pages, single-column) describing original research. These papers will undergo a standard reviewing process and if accepted, they will be included in the proceedings with ISBN.
- **Abstracts** (up to four pages, single-column) of author's earlier published and successfully presented papers (at conferences, journals, etc.). These contributions will not be reviewed; emphasis will be put on the presentation and discussion. The abstracts will be included in the proceedings with ISBN.
- **Student posters** – abstracts of defended Bc. and MSc. Theses with subsequent poster presentation. These abstracts will not be included in the Proceedings.

SCOPE
The PESW workshop addresses emerging issues, hot problems, new solution methods, and their hardware and software implementations in the fields of digital and mixed-signal system design. It is especially focused on dependable and low power design, and testing methods related to the SoC technology and modern embedded applications. The workshop topics include (but are not limited to):

- Programmable/re-configurable/adaptable architectures
- SoC and NoC design and testing
- Digital design optimization methods, logic synthesis and optimization
- Architectures and hardware for security applications
- On-line and off-line error detection and correction
- Fault-tolerant systems design methods, testability and reliability analysis
- Diagnosis & testing of embedded systems
- Verification of embedded systems
- Mission-critical & real-time systems
- Heterogeneous Cyber-Physical Systems, robotic control
- Applications of (embedded) digital systems

Special Session on Network Security:
- Network monitoring and measurements
- Network traffic processing and analysis, anomaly detection
- Security and privacy issues
- Security issues of Internet of Things

SUBMISSION AND PRESENTATION
Prospective authors are encouraged to submit their contributions electronically through the page https://www.easychair.org/conferences/?conf=pesw2019 or (in case of access problems) by e-mail to Workshop Chairs **before the submission deadline**.
Accepted full papers, abstracts, and student posters are supposed to be presented at PESW 2019 by the authors. Submitted abstracts will have longer presentations (20 min. presentation + 10 min. discussion).
The Proceedings will be distributed electronically and provided with ISBN. The list of papers and abstracts will be published at the PESW web page.

IMPORTANT DATES

Submission of full papers:	**May 20, 2019 (final deadline)**
Submission of abstracts:	**May 20, 2019 (final deadline)**
Notification of acceptance of full papers and abstracts:	June 3, 2019
Final versions of full papers and abstracts:	June 10, 2019
Submission of abstracts of student posters:	June 23, 2019
Notification of acceptance of student posters:	June 25, 2019
Workshop:	June 27–29, 2019