By **CTU in Prague**
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Call for papers
8th Prague Embedded Systems Workshop 2020
PESW 2020
November 6–8, 2020, Horoměřice, Czech Republic (+ on-line)
**A forum for friendly discussion of emerging ideas
aimed at doctoral and prospective doctoral students**

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CONTACT INFORMATION
HANA KUBÁTOVÁ
PETR FIŠER
Department of Digital Design
Faculty of Information Technology
Czech Technical University in Prague
Thákurova 9, Praha 6, 160 00
Czech Republic
Tel.: +420 224 359 840
e-mail: kubatova@fit.cvut.cz, fiserp@fit.cvut.cz

WORKSHOP WEB PAGE
http://pesw.fit.cvut.cz/

BACKGROUND
The Prague Embedded Systems Workshop is a research meeting intended for the presentation and discussion of students' results and progress in all aspects of embedded systems design, testing, and applications. It is organized by members of the Department of Digital Design at Faculty of Information Technology (which is the youngest faculty) of the Czech Technical University in Prague (which is the oldest technical university in Central Europe). The workshop aims to enhance collaboration between different universities not only inside the EU. It will be based on oral presentations, mutual communication, and discussions.

SCOPE
The PESW workshop addresses emerging issues, hot problems, new solution methods, and their hardware and software implementations in the fields of digital and mixed-signal system design. It is especially focused on dependable and low power design, and testing methods related to the SoC technology and modern embedded applications. The workshop topics include (but are not limited to):

- Programmable/re-configurable/adaptable architectures
- SoC and NoC design and testing
- Digital design optimization methods, logic synthesis and optimization
- Architectures and hardware for security applications
- On-line and off-line error detection and correction
- Fault-tolerant systems design methods, testability and reliability analysis
- Diagnosis & testing of embedded systems
- Verification of embedded systems
- Mission-critical & real-time systems
- Heterogeneous Cyber-Physical Systems, robotic control
- Applications of (embedded) digital systems

Special Session on Network Security:
- Network monitoring and measurements
- Network traffic processing and analysis, anomaly detection
- Security and privacy issues
- Security issues of Internet of Things

SUBMISSION TYPES
- **Full papers** (up to 10 pages, single-column) describing the student's original research. These papers will undergo a standard reviewing process and if accepted, they will be included in the proceedings with ISBN.
- **Abstracts** (up to four pages, single-column) of author's earlier published and successfully presented papers (at conferences, journals, etc.). These contributions will not be reviewed; emphasis will be put on the presentation and discussion. The abstracts will be included in the proceedings with ISBN.
- **Student posters** – abstracts of defended Bc. and MSc. Theses with subsequent poster presentation. These abstracts will not be included in the Proceedings.

SUBMISSION AND PRESENTATION
Prospective authors are encouraged to submit their contributions through the page https://www.easychair.org/conferences/?conf=pesw2020 **before the submission deadline**.
Accepted full papers, abstracts, and student posters are supposed to be presented at PESW 2020 by the authors. Submitted abstracts will have longer presentations (20 min. presentation + 10 min. discussion).
The Proceedings will be distributed electronically and provided with ISBN. The list of papers and abstracts will be published at the PESW web page.

IMPORTANT DATES

Submission of full papers:	**October 21, 2020 (final)**
Submission of abstracts:	**October 21, 2020 (final)**
Notification of acceptance of full papers and abstracts:	October 23, 2020
Final versions of full papers and abstracts:	October 26, 2020
Submission of abstracts of student posters:	**October 21, 2020 (final)**
Notification of acceptance of student posters:	October 23, 2020
Workshop:	November 6-8, 2020