



By CTU in Prague
in cooperation with



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WORKSHOP WEB PAGE

<http://pesw.fit.cvut.cz/2017>

5th Prague Embedded Systems Workshop 2017

PESW 2017

June 29–30, 2017, Roztoky u Prahy, Czech Republic

**A forum for friendly discussion of emerging ideas
aimed at doctoral and prospective doctoral students**

BACKGROUND

The Prague Embedded Systems Workshop is a research meeting intended for the presentation and discussion of students' results and progress in all aspects of embedded systems design, testing and applications. It is organized by members of the Department of Digital Design at Faculty of Information Technology (which is the youngest) of the Czech Technical University in Prague (which is the oldest technical university in Central Europe). The workshop aims to enhance collaboration between different universities not only inside EU. It will be based on oral presentations, mutual communication and discussions.

SUBMISSION TYPES

- **Full papers** (up to 10 pages, single-column) describing original research.
- **Abstracts** (one page, single-column) of earlier published successfully presented papers (at conferences, journals, etc.).
- **Student posters** – abstracts of defended Bc. and MSc. Theses with subsequent poster presentation. Will not be included in the Proceedings.

SCOPE

The workshop PESW 2017 addresses emerging issues, hot problems, new solution methods, and their hardware and software implementations in the fields of digital and mixed-signal system design. It is especially focused on dependable and low power design, and testing methods related to the SoC technology and modern embedded applications. The workshop topics include (but are not limited to):

- Programmable/re-configurable/adaptable architectures
- SoC and NoC design and testing
- Digital design optimization methods
- Architectures and hardware for security applications
- On-line and off-line error detection and correction
- Fault-tolerant control systems design methods
- Testability and reliability analysis
- Logic synthesis and optimization
- Diagnosis & testing of embedded systems
- Verification of embedded systems
- Mission-critical & real-time systems
- Heterogeneous Cyber-Physical Systems
- Robotic control
- Applications of (embedded) digital systems.

Special Session on Network Security:

- Network monitoring and measurements
- Network traffic processing and analysis, anomaly detection
- Security and privacy issues

SUBMISSION AND PRESENTATION

Prospective authors are encouraged to submit their contributions electronically through the page <https://www.easychair.org/conferences/?conf=pesw2017> or (in case of access problems) by e-mail to Workshop Chairs **before the submission deadline**.

Accepted full papers, abstracts, and student posters are supposed to be presented at PESW 2017 by the authors. Submitted abstracts will have longer presentations (30 min. with discussion).

The Proceedings will be distributed electronically (flash drive) and provided with ISBN. The list of papers and abstracts will be published at the PESW web page.

IMPORTANT DATES

Submission of papers:

May 20, 2017 (extended)

Notification of acceptance:

June 11, 2017

Deadline for final versions of papers and abstracts:

June 21, 2017

Student posters submission deadline:

June 24, 2017

Student posters notification of acceptance:

June 26, 2017

Workshop:

June 29–30, 2017