Programme

Tuesday May 21	13:30	ESW opening Presentations in Sessions **Dependability** • Hierarchical Dependability Models Based on Markov Chains • Self-Repair Modified Duplex System Architecture based on Reconfigurations **Theory** • XOR and Equivalence operators in Non-Exact Concept Learning	Kohlík Borecký, Vít Rosensweig
	15:30	Coffee break	
	16:00	**Applications** • Applications of digital systems • New Approaches to a Connection of Memory Consistency and Cache Coherence • Hardware Support For Precise Time and Frequency Distribution **Faults in FPGA** • FPGA bitstream SEU effect emulation and evaluation • A New Method of Modeling Radiation-Induced Soft Errors in FPGA by Architecture Analysis	Mróz, Zając, Pławiak-Mowna Kašpar Dostál Kvasnička Pospíšil, Vaňát
	18.30	Dinner	
	20.00	PC meeting	

Wednesday May 22			
	8.00	Breakfast	
	9.00	**Networking** • Real-time Network Measurement for High-Speed Networks • Real-time Detection of Anomalies in High-speed Computer Networks • The Behavior Network (BeNet) **Testing** • Test Pattern Decompression in Parallel Scan Chain Architecture	Benáček Čejka Šlechta Chloupek
	12.00	Lunch	
	13:30	Closing discussion	